

ptb_regs

Version: 1.0

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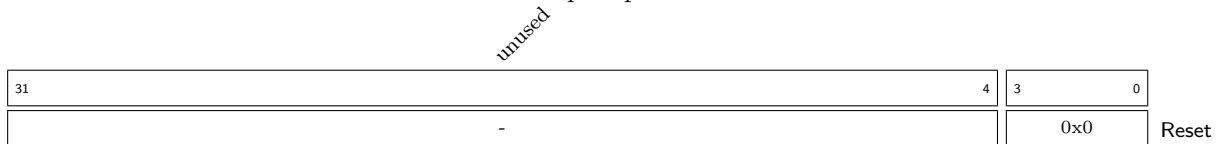
PTB-unique registers for ALPIDE tests.

1 Register List

#	Name	Mode	Address	Type	Length	Reset
0	top_chip_id	RW	0x00000000	SLV	4	0x0
1	bot_chip_id	RW	0x00000001	SLV	4	0x0

2 Registers

Register 2.1: TOP_CHIP_ID - RW (0x00000000)
Top chip ID.



Register 2.2: BOT_CHIP_ID - RW (0x00000001)
Bottom chip ID.

