

Threshold Scan

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1 Threshold scan

The Threshold scan injects multiple hits, with increasing charge, directly into the in-pixel memories of the ALPIDE. A range of charges is chosen and injected into the in-pixel memory. When a pixel fires more than 50% of the time for a given charge the threshold is identified. A section of pixels is selected, and is injected.

1.1 Alpile Registers

- `reg_fromu_config1 (0x4)`

Must be set to 0x20 to get analog pulsing.

- `reg_fromu_config2 (0x5)`

Strobe duration. The length of the window that a hit can be latched onto. Default: 0x14

- `reg_fromu_config3 (0x6)`

Strobe gap. The gap between two strobes with the internal strobe sequencer.

- `reg_fromu_pulsing1 (0x7)`

Pulse to strobe delay. The gap between the generation of a pulse and the generation of a strobe signal. Must be less than the duration of a strobe in order to collect the signal.

- `reg_fromu_pulsing2 (0x8)`

The duration of a pulse signal. If it is set to 0, no pulse will be generated.

- `reg_cmudmu_config` (0x10) Can be used to enable manchester encoding. For threshold scan performed on the VCU118 this must be turned on, which is the default value. For PTB it must be turned off.
- `reg_vpulsel` (0x605)

Determines the charge that is injected into the pixel. For the threshold scan this value is typically incremented over a range of 50 DAC units.

1.2 Registers to be set on board

- `reset_counters`

Found in ALPIDE CONTROL. Resets control counters.

- `enable_frame_based_offload`

Found in OFFLOAD. Forces the offload to be sorted so that data from two different frames are not entangled.

- `enable_offload`

Found in OFFLOAD. Enables the offload state machine. Register must be set to 1 in order to receive data.

- `enable_alpide_clock`

Found in GLOBAL REGS. Turns on the Alpide clock. Register must be set to 1 in order to turn on Alpide. Reset to default value of 0 at the end of the test.

- `reset_offload_fifo`

Found in OFFLOAD. Resets the offload FIFO. Is a PULSE, so the register should be set to 1 in order to empty the FIFOs.

- `run_settings`

Found in ALPIDE DATA. Register must be set to 1 in order to put the data-module into run mode.

- `trigger_source`

Found in TRIGGER MANAGER. The source of the ALPIDE trigger signal.

- `alpide_mode`

Found in TRIGGER MANAGER. For triggered mode set register to 0.
For continues mode set register to 1.

- mode

Found in TRIGGER MANAGER. Controls whether transmitting trigger or pulse. Register must be set to 2 for digital scan.

- num_triggers

Found in TRIGGER MANAGER. Sets the number of triggers.

- pulse_delay

Found in TRIGGER MANAGER. Sets the period between the pulses. Is an increment of 25 ns. So for 10 μ s between pulses, set the register to 400. Must at minimum be pulse_trigger_delay + 14

- pulse_trigger_delay

Found in TRIGGER MANAGER. Sets the delay between the pulse signal and the trigger signal. Must at minimum be 14-num_triggers.

- trigger_init

Found in TRIGGER MANAGER. Initiates the command transmission process for all modes.

In addition to this, the pixels that are to be pulsed must be selected and unmasked.

Detailed information on registers can be found in [wp3/firmware/source/modules](#)